SAMPLE ABSTRACT
[Include all text within the margins shown]

PROJECT TITLE

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CO-PRINCIPAL INVESTIGATOR(S):

COOPERATING COMPANY(S): Sponsor(s)

ABSTRACT

This work concentrates on extending our previous research in two areas, which are some of the most topical challenges at the frontier of analog IC design. The first research effort described is on a new architecture proposed for the implementation of a CMOS 10 bit, 100 MS/s A/D converter, which would advance the state of the art of A/D conversion in this technology, and enable emerging applications such as high definition image processing and medical signal processing. The second research project extends our previous work on low-power, high speed signal processors *for* disk drive read channels to adaptive equalization and clock recovery in partial response, maximum likelihood detection systems, two of the most challenging functions in realizing the improvements offered by these advanced signal processors. We believe that analog implementations of these read channels will offer the lowest power dissipation, making them suitable *for* use in battery operated computers.

Keywords: ADC, CMOS Mixed Signal, Read-Channel, Equalizer, Disc Drive